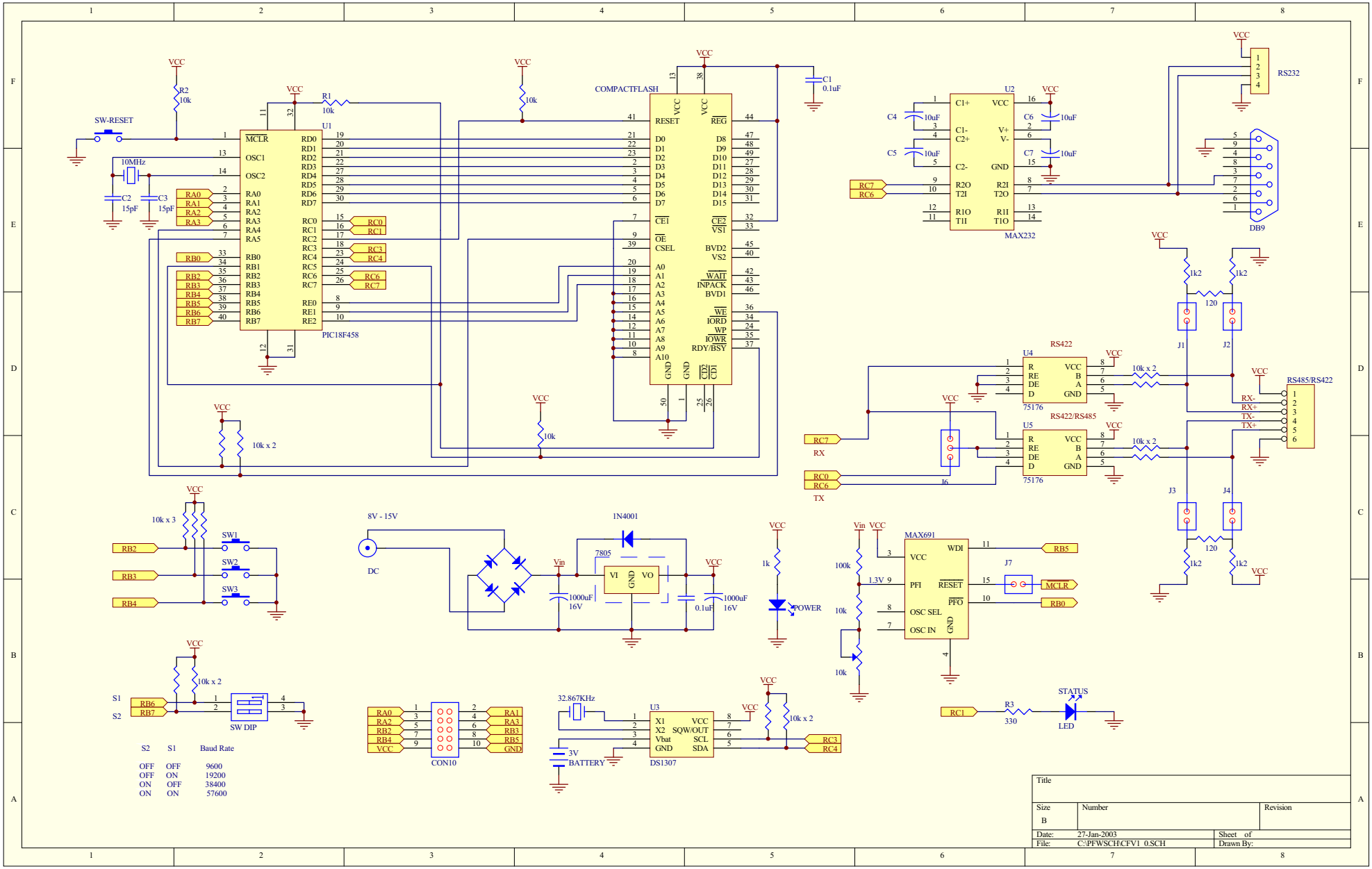
VCC
R2
10k
SW-RESET
U1
MCLR
OSC1
OSC2
RD0
RD1
RD2
RD3
RD4
RD5
RD6
RD7
RA0
RA1
RA2
RA3
RA4
RA5
RC0
RC1
RC2
RC3
RC4
RC5
RC6
RC7
RB0
RB1
RB2
RB3
RB4
RB5
RB6
RB7
RE0
RE1
RE2
PIC18F458
10MHz
C2
15pF
C3
15pF
R1
10k
10k x 2
COMPACTFLASH
RESET
REG
D0
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
D12
D13
D14
D15
CE1
CE2
VS1
OE
CSEL
VS2
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
WAIT
INPACK
BVD1
WE
IORD
WP
IOWR
RDY/BSY
BVD2
GND
CD2
CD1
C1
0.1uF
U2
C1+
C1-
C2+
C2-
V+
V-
R2O
T2I
R1O
T1I
R2I
T2O
R1I
T1O
MAX232
C4
C5
C6
C7
10uF
RS232
DB9
RS422
U4
R
RE
DE
D
75176
RS422/RS485
U5
RS485/RS422
RX-
RX+
TX-
TX+
RC7
RX
RC0
RC6
TX
J1
J2
J3
J4
1k2
120
J6
SW1
SW2
SW3
10k x 3
8V - 15V
DC
1N4001
7805
VI
VO
1000uF
16V
0.1uF
POWER
1k
MAX691
WDI
PFI
RESET
PFO
OSC SEL
OSC IN
Vin
1.3V
100k
J7
MCLR
RB0
RB5
STATUS
LED
R3
330
RC1
S1
S2
SW DIP
S2 S1 Baud Rate
OFF OFF 9600
OFF ON 19200
ON OFF 38400
ON ON 57600
CON10
32.867KHz
3V
BATTERY
U3
X1
X2
Vbat
GND
VCC
SQW/OUT
SCL
SDA
DS1307
RC3
RC4
Title
Size
B
Number
Revision
Date: 27-Jan-2003
Sheet of
File: C:\PFWSCH\CFV1 0.SCH
Drawn By: