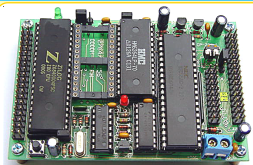
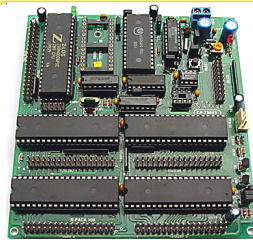


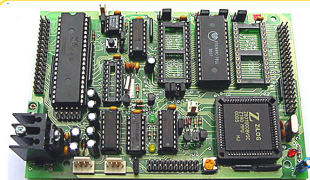
**CP-Z80 V1 (P-CP-A-00074)**

- CPU Z80B CMOS/RUN 4 MHz
- 8 K RAM ON BOARD
- 8K / 32 K EPROM MONITOR
- 40 PIN Z80 BUS
- 24 BIT I/O 82C55 PORT 34 PIN
- 20 PIN LCD
- POWER DC +5 VDC
- PCB SIZE 9 x 6 CM.
- DEVELOP PROGRAM WITH ET-EPROM EMULATOR 8/32 OR ET-BOARD V3.5/V4.0/V5.0/V6.0

• CP-Z80 V1.0 INCLUDES; BOARD AND UAER MANUAL

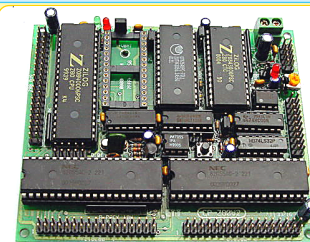
**CP-Z80 V3 (P-CP-A-00003)****CP-Z80 V3 PLUS (P-CP-A-00004)**

- CPU Z80B CMOS/RUN 4 MHz
- 8 K / 32 K RAM ON BOARD 8K (6264)
- 8K / 32 K EPROM MONITOR (2764/27256)
- 96 BIT I/O 8255 PORT 34 PIN x 4
- DS1232 (POWER ON RESET/WATCHDOG) EEPROM
- 20 PIN LCD PORT (CLCD, GLCD MODULE)
- RTC 6264 (CP-Z80V3 PLUS), 40 PIN Z80
- PCB SIZE 12 x 13.5 CM., POWER DC +5VDC
- DEVELOP PROGRAM WITH ET-EPROM EMULATOR 8/32 OR ET-BOARD V3.5/V4.0/V5.0/V6.0
- CP-Z80 V3.0 AND CP-Z80 V3 PLUS INCLUDE; BOARD AND USER MANUAL

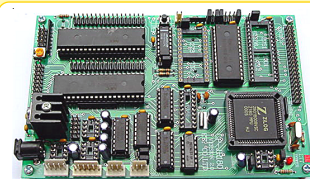
**CP-jr180 (P-CP-A-00006)****CP-jr180 PLUS (P-CP-A-00007)**

- CPU Z80180 CMOS/RUN 6.144 MHz
- 32K/64K EPROM MONITOR (27256/27512)
- 24 BIT I/O 8255 PORT 34 PIN ET x 1
- 20 PIN LCD/POWER ON RESET/WATCH DOG
- RS232 PORT 2-CH. (Z80180), 40 PIN Z80 BUS
- PCB SIZE 9 x 12 CM.
- POWER 7805 POWER DC 7-12VDC
- WRITING AND DEVELOP PROGRAM WITH ET-DEBUGGER JR 180 OR ET-EPROM EMULATOR

• CP-JR180, CP-JR180 PLUS INCLUDE; BOARD AND USER MANUAL

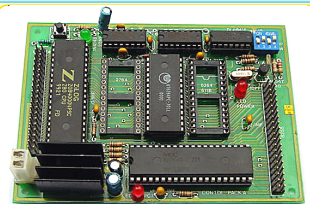
**CP-Z80 V2 (P-CP-A-00002)**

- CPU Z80B CMOS/RUN 4 MHz
- 8 K / 32 K RAM ON BOARD 8K (6264)
- 8K / 32 K EPROM MONITOR (2764/27256)
- 48 BIT I/O 8255 PORT 34 PIN x 4
- Z80ACTC/WATCH DOG/POWER ON RESET
- 20 PIN LCD PORT
- PCB SIZE 11 x 9.6 CM.
- 40 PIN Z80 BUS, POWER DC +5 VDC
- DEVELOP PROGRAM WITH ET-EPROM EMULATOR 8/32 OR ET-BOARD V3.5/V4.0/V5.0/V6.0
- CP-Z80 V2.0 INCLUDES; BOARD AND USER MANUAL

**CP-jr180 V2 (P-CP-A-00008)**

- CPU Z80180-8 CMOS RUN 6.144 MHz (XTAL 12.288 MHz)
- 32 K RAM ON BOARD(62256)
- 32 / 64 K ROM (27256 /27512)
- 512 K ROM/RAM (27515/271001/62256/62128/62512)
- 48 BIT I/O PORT 8255 34 PIN ET-BUS x 2

- 20 PIN LCD ET-BUS (CLCD, GLCD)
- 10 PIN KEYBOARD (MATRIX 4 x 4 0, 10 PIN SDP8)
- RS232 PORT 2-CHANNEL
- RTC 6242 (OPTION)
- WATCH DOG & POWER ON RESET & BACK UP (MAX 691)
- A-TO-D 12 BIT 2-CHANNEL LTC1298 (OPTION)
- 7805 POWER SUPPLY ONBOARD
- WRITING AND DEVELOP PROGRAM WITH ET-DEBUGGER JR 180 OR ET-EPROM EMULATOR
- CP-JR180 V2.0 INCLUDES; BOARD,CD-ROM, USER MANUAL
- RS422/485 (OPTION)
- MINI SPEAKER ON BOARD
- PCB SIZE 10 x 16 CM.

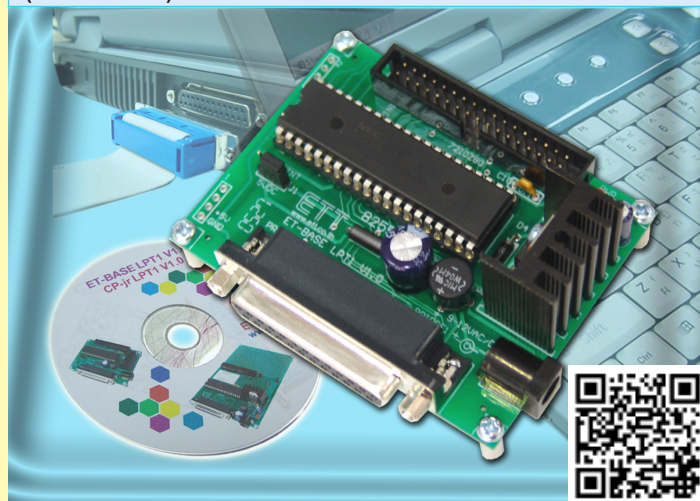
**CP-Z80CPA (P-CP-A-00005)**

- CPU Z80A/RUN 3.579 MHz
- 8 K RAM ON BOARD (6264)
- 8 K EPROM MONITOR (2764, 2732)
- 8 K RAM EXPANSION (6116, 6264)
- 24 BIT I/O 8255 PORT 40 PIN
- 40 PIN Z80 BUS
- 7805 ON BOARD POWER 7-12VDC
- PCB SIZE 12 X 8.5 CM.
- DEVELOP PROGRAM WITH ET-EPROM EMULATOR OR ET-BOARD V3.5/V4.0/V5.0/V6.0

• Z80-CPA INCLUDES; BOARD AND USER MANUAL

**ET-BASE LPT1 V1.0**

(P-ET-A-00248)

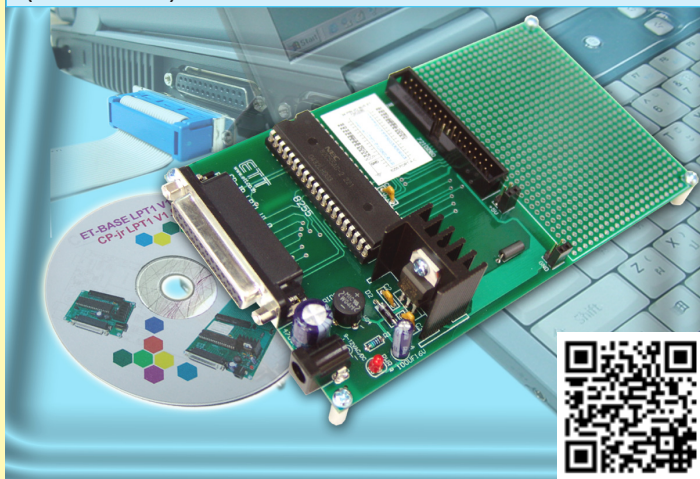


Board I/O that uses IC No.8255 has 3 PORT or 24 BIT to be interfaced with computer PC through Connector PRINTER PORT 25 PIN (not be interfaced through CONVERTER USB TO PARALLEL), so it make us interface Board with computer PC easier. It is suitable for using computer PC to be controller or test writing simple program to control through computer PC.

- 1 PORT IC 8255, 3 PORT FOR (24 BIT) INPUT/OUTPUT TTL
- 1 CONNECTOR 34 PIN HEADER BOX for interfacing with ETT BOARD'S COMPONENTS such as RELAY BOARD (ET-REL8, ET-INOUT24), SSRAC (ET-SSRAC 4, ET-SSRAC, ET-SSRAC V2)
- CONNECTOR DB 25 PIN FEMALE for interfacing PAIR CABLE with PRINTER PORT of computer PC
- POWER SUPPLY 7805 ON BOARD using with POWER SUPPLY DC/AC 9 - 12V (OPTION)
- PCB SIZE 6.2 x 8.1 cm.
- CAN BE USED WITH BOARD ET-BASIC I/O V1
- ET-BASE LPT V.0 consists of
  1. BOARD ET-BASE LPT1 V1.0
  2. CD-ROM User's Manual and EXAMPLE of written VISUAL BASIC PROGRAM
  3. CABLE 25 PIN HEADER

**CP-jrLPT1 V1.0**

(P-CP-A-00076)



It is a Board that has the same specifications as version ET-BASE LPT V.0 but there is additional PCB (Prototype Working Area).

- PCB SIZE 15.3 x 9 cm.
- PCB (Prototype Working Area) SIZE 8.5 x 6 cm.
- CP-jrLPT1 V1.0 consists of
  1. BOARD CP-jrLPT1 V1.0
  2. CD-ROM User's manual and EXAMPLE of written VISUAL BASIC PROGRAM
  3. CABLE 25 PIN HEADER

