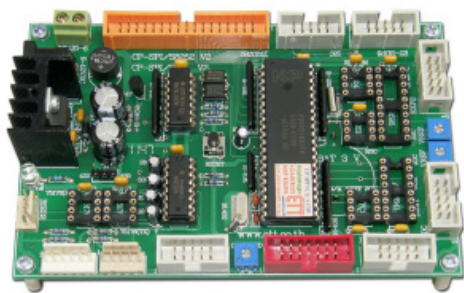


CP-SPI/RD2 V2

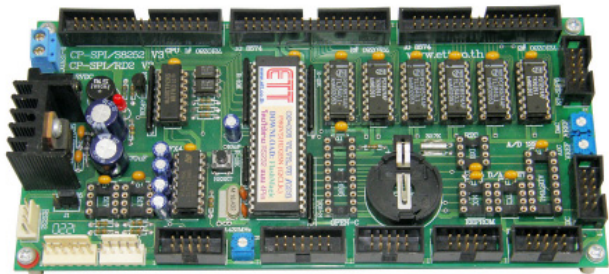
(P-CP-A-00041)



- CPU P89V51RD2 64 KBYTE PROGRAM FLASH MEMORY, 1 KBYTE RAM IN CPU, RUN 6 CLOCK 1 INSTRUCTION
 - RUN 18.432 MHz ON BOARD
 - 24 BIT I/O PORT CPU IN 34 PIN ET-BUS
 - 14 PIN CHARACTER LCD PORT
 - RS232 IC MAX232 ON BOARD/RS422/485 USING IC 75176 (OPTION)
 - RTC USING IC NO. DS1307 (OPTION)
 - EEPROM NO. 24XX (24C16(2K)-24C256(32K) AS OPTION)
 - 4-CH. 12 BIT A-TO-D USING IC NO.ADS7841 (OPTION)
 - 2-CH. 10 BIT D-TO-A USING IC NO.LTC1661 (OPTION)
 - POWER LOGIC 8 BIT AS HIGH-VLTAGE OPEN COLLECTOR USING WITH 200 mA SINK AND USING IC NO. NC6B595 (OPTION)
 - ET-SDP8 BUS, ET-12C BUS, 7805 POWER SUPPLY ON BOARD
 - 48 BIT I/O PORT BY USING IC PCF8574 2-CH. 34 PIN ET-BUS
 - PCB SIZE 13 x 8.3 CM.
 - POER SUPPLY 7-12VDC
 - CP-SPI/RD2 V2 INCLUDES;
1. CP-SPI/RD2 V2 BOARD
 2. CABLE DOWNLOAD ET-RS232 DB 9 PIN
 3. CD-ROM

**CP-SPI/RD2 V3**

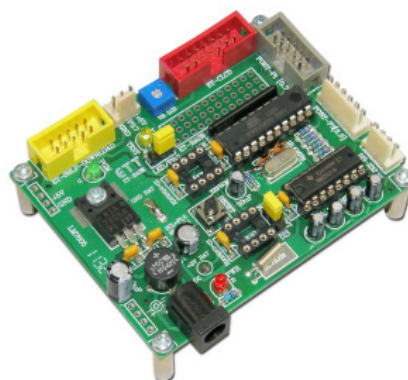
(P-CP-A-00043)



It is expanded I/O from CP-SPI V2 and PORT 12C NO. PCF8574 expands 6ports additionally, so it is 48 bit.

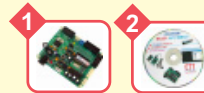


- CPU P89V51RD2 64 KBYTE PROGRAM FLASH MEMORY, 1 KBYTE RAM IN CPU, RUN 6 CLOCK 1 INSTRUCTION
 - RUN 18.432 MHz ON BOARD
 - 24 BIT I/O PORT CPU IN 34 PIN ET-BUS
 - 48 BIT I/O PORT BY USING IC PCF8574 2-CH. 34 PIN ET-BUS
 - 14 PIN CHARACTER LCD PORT
 - RS232 IC MAX232 ON BOARD/RS422/485 USING IC 75176 (OPTION)
 - RTC USING IC NO. DS1307 (OPTION)
 - EEPROM NO. 24XX (24C16(2K)-24C256(32K) AS OPTION)
 - 4-CH. 12 BIT A-TO-D USING IC NO.ADS7841 (OPTION)
 - 2-CH. 10 BIT D-TO-A USING IC NO.LTC1661 (OPTION)
 - POWER LOGIC 8 BIT AS HIGH-VLTAGE OPEN COLLECTOR USING WITH 200 mA SINK AND USING IC NO. NC6B595 (OPTION)
 - ET-SDP8 BUS, ET-12C BUS, 7805 POWER SUPPLY ON BOARD
 - PCB SIZE 17.5 x 8.3 CM.
 - POER SUPPLY 7-12VDC
 - CP-SPI/RD2 V3 INCLUDES;
1. CP-SPI/RD2 V3 BOARD
 2. CABLE DOWNLOAD ET-RS232 DB 9 PIN
 3. CD-ROM

**ET-BASE LP4052 V1 (P-ET-A-00255)**

BOARD MCS51 uses IC No.AT89LP4052 to be permanent and high speed CPU on board. The standard Board ET-BASE Size can be used with ET-BASE I/O V1.

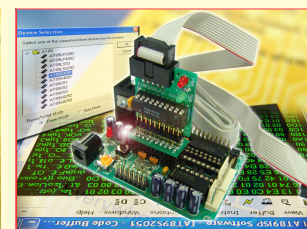
- CPU AT89LP4052, RUN 18.432MHz, 4KBYTE FLASH PROGRAM, 256BYTE RAM
 - 15 BIT I/O STANDARD 10 PIN ET, 6 PIN HEADER
 - RS232 PORT ON BOARD, RTC DS1307 (OPTION), EEPROM 24XX (OPTION)
 - 14 PIN LCD PORT
 - 7805 ON BOARD POWER SUPPLY
 - POER SUPPLY 7-12VDC
 - PCB SIZE 6.2 x 8.1 cm.
 - 10 PIN ET-89LP DOWNLOAD can be written program and DOWNLOAD into CPU directly without using any COPY PROGRAMMER (using with ET-CAP LP4052)
 - ET-BASE LP4052 V1 consists of
1. ET-BASE LP4052 V1 BOARD
 2. CD-ROM

**ET-CAP LP4052 (P-CB-A-00018)**

ET-CAP LP4052 which is the compact set with cable DOWNLOAD for using with Board that uses Connector 10 PIN ET-89LP. To interface with computer PC through DB 25 PIN PRINTER PORT and uses with program ATMEL "AT89ISP" for running on WINDOWS 98/ME/2000/XP.

ET-EM89LP-20P (P-ET-A-00256)

ET-EM89LP-20 which is the compact set to write and develop program can be used instead of CPU MCS51 20 PIN DIP TYPE from ATMEL. To write program on computer PC then transforms data into INTEL HEX and Download written program into CPU directly without taking off any CPU. It is very quickly and safe power.



- CPU AT89LP4052 20 PIN DIP TYPE, 4KBYTE FLASH MEMORY
 - Using instead of CPU 20PIN DIP No.AT89C2051, C4051, AT89S2051, S4051.
 - Interface with Computer PC through Printer Port 25 PIN and run on Windows 98/ME/2000/XP
 - ET-EM89LP-20 consists of
1. BOARD ET-EM89LP-20
 2. ET-CAP LP4052
 3. CD-ROM User's Manual and PROGRAM

